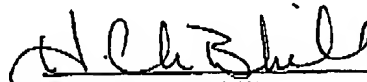


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Date: August 10, 2005
Hui Chin BarnhillIn re application of: **Tu**Confirmation No.: **3555**U.S. Serial Number: **10/757,203**Art Unit: **2826**Filing Date: **January 14, 2004**Examiner: **Wilson, Scott R.**Our Reference Number: **252016-1940**

Title: **Novel Random Access Memory (RAM) Capacitor in Shallow Trench
Isolation with Improved Electrical Isolation to Overlying Gate Electrodes**

Issue Fee Transmittal**Credit Card Authorization Form (Amount: \$1700.00)****Total Pages Transmitted (including cover sheet) - 3**

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000047390 7590 05/19/2005

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Hui Chin Barnhill

(Depositor's name)

Hui Chin Barnhill

(Signature)

August 10, 2005

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/757,203	01/14/2004	Kuo-Chi Tu	TS03-265	3553

TITLE OF INVENTION: NOVEL RANDOM ACCESS MEMORY (RAM) CAPACITOR IN SHALLOW TRENCH ISOLATION WITH IMPROVED ELECTRICAL ISOLATION TO OVERLYING GATE ELECTRODES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	08/19/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
WILSON, SCOTT R	2826	257-301000	01 FC:1501 02 FC:1504	00000002	10757203
					1400.00 OP 300.00 OP

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1 Thomas, Kayden,

2 Horstemeyer & Risley

3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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TAIWAN SEMICONDUCTOR
MANUFACTURING CO., LTD.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

HSINCHU, TAIWAN, R.O.C.

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Daniel R. McClure

Date Aug 10, 2005

Typed or printed name Daniel R. McClure

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